METHOD AND APPARATUS FOR OPERATING VIRTUAL COMPUTER

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Abstract

The loop part repeated during execution of an intermediate code (111) is detected dynamically during the execution and the intermediate code of the loop part is converted into a native code (112). The native code (112) is executed directly by means of a CPU (10) for the loop part of the intermediate code. More specifically, an instruction for directing the control flow upward is detected when the intermediate code (111) is loaded in a memory (11) and rewritten into a first special instruction when it is detected. When the intermediate code (111) in the memory (11) is executed, the jump destination of control is confirmed before the first special instruction is executed. If the jump destinations of control agree with each other when the same first special instruction is executed again, the jump destination judges that the branch point is the loop part and converts the bite code (111) of the loop part is converted into the native code (112). The time required to convert the intermediate code (111) into the native code (112) is shortened and the overall execution speed is enhanced even in an apparatus having a relatively small memory capacity.